

**AMENDED REPLACEMENT CLAIMS SHOWING AMENDMENTS MADE THERETO**

1. (AMENDED) A system for prewriting a video frame in a liquid crystal display, said system comprising:

a liquid crystal display (LCD) having a matrix of liquid crystal pixels, said matrix further divided into a plurality of sub-matrices of pixels;

at least one [analog-to-digital converter (ADC)] digital-to-analog converter (DAC) adapted to receive a digital input representative of an analog voltage and having an analog output adapted for applying the analog voltage to at least one of the pixels at a time;

a plurality of column switches adapted for coupling the analog output of said at least one [ADC] DAC to at least one of a plurality of columns of said LCD;

a plurality of row switches adapted for selectively coupling the plurality of columns to the pixels of said LCD;

logic circuits for calculating an average voltage value for each of the plurality of sub-matrices from final voltage values associated with the pixels of each of the sub-matrices; and

logic circuits for controlling the plurality of column switches and the plurality of row switches so that each sub-matrix may be precharged with its calculated average voltage value, then each of the pixels charged with the final voltage value representative of that portion of the video frame represented by that pixel.

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21. (AMENDED) A liquid crystal display (LCD), comprising:

a matrix of liquid crystal pixels, said matrix further divided into a plurality of sub-matrices of pixels;

at least one [analog-to-digital converter (ADC)] digital-to-analog converter (DAC) adapted to receive a digital input representative of an analog voltage and having an analog output adapted for applying the analog voltage to at least one of the pixels at a time;

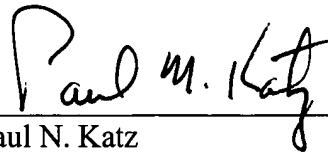
a plurality of column switches adapted for coupling the analog output of said at least one [ADC] DAC to at least one of a plurality of columns of said LCD;

a plurality of row switches adapted for selectively coupling the plurality of columns to the pixels of said LCD;

logic circuits for calculating an average voltage value for each of the plurality of sub-matrices from final voltage values associated with the pixels of each of the sub-matrices; and

logic circuits for controlling the plurality of column switches and the plurality of row switches so that each sub-matrix may be precharged with its calculated average voltage value, then each of the pixels charged with the final voltage value representative of that portion of the video frame represented by that pixel.

Respectfully submitted,

A handwritten signature in black ink, reading "Paul N. Katz". The signature is written in a cursive style with a large initial "P" and "K".

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Paul N. Katz

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